

A: To stop it, try to run the program as an Administrator. If that doesn't work (your permissions could be off), you can disable that feature in the Vista Settings. 1. Field of the Invention The present invention relates to parallel processing, and, more particularly, to a multiple processor system with unified power management. 2.

Description of the Related Art Current parallel processing (multiprocessing) systems use various forms of circuitry to distribute data to and from the parallel processing units or "CPUs."

For example, one such parallel processing system includes a number of nodes which are connected by various data buses. Each node has a number of CPUs and a number of input/output processors that are connected to the data buses. The data buses are separate from the power buses and are used to transmit data between the nodes. The data buses are also used to transmit power to the nodes. Another such parallel processing system includes a number of nodes connected together by a number of bus lanes, each of which carries power to a node. In this system, each node also has a number of CPUs and I/O processors which are connected to the bus lanes. In one implementation, each node is a monolithic system on a single integrated circuit. In that system, each node has a CPU, an I/O processor, an on-chip microcontroller, and memory which are connected to a single chip package. The on-chip microcontroller is used to control the on-chip device functions on the chip, e.g., a direct memory access (DMA) engine, a microprocessors, I/O interfaces and an interconnect. The on-chip microcontroller also controls the nodes through the bus lanes. Each CPU within the node has its own microcontroller and memory. The on-chip microcontroller controls and interfaces to these components. The on-chip microcontroller also controls the components within the node. The components

within the node, including the microcontroller, have the same clocks as those within the node's CPU. The node's I/O processors are separate from the node's CPU and its microcontroller and have their own clocks. The microcontroller and I/O processors share a common microcontroller clock line. The parallel processing system may also include different types of nodes. In one such implementation, the system includes the type of node described above with the exception that the node includes two CPUs, one for use when the node has no other work to do, i

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